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## APPLICANTS

Osamu Ichikawa, Takatsuki-shi, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-254181 08/30/2002

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
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## ADDRESS

STEVENS, DAVIS, MILLER &amp; MOSHER, L.L.P.

Suite 850

1615 L Street, N.W.

Washington, DC

20036

## TITLE

Semiconductor integrated circuit and memory test method

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